

ABSTRACT OF THE DISCLOSURE

This invention provides a driving circuit for a display device, for accomplishing low power consumption by use of a level shifter capable of reliably converting signals having low voltage amplitude. A source signal line driving circuit is divided into a plurality of units. Pulses outputted from a shift register, etc, are used to execute ON/OFF control of the operation of a current supply source to the level shifter of each divided unit. The supply of the current to the level shifter is suspended in the stage not containing the circuit that outputs the pulses, but is made to only the unit including the shift register of the operating stage. In consequence, low power consumption can be achieved.

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